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ABSTRACT OF DISCLOSURE

An apparatus and a method of generating a video-reproducing clock signal from a 480p signal. The apparatus generates a video-reproducing clock signal from a 480p signal that includes a vertical synchronization signal, horizontal synchronization signals, and copy guard signals. The apparatus includes a coast signal generating unit and a clock signal generating unit. The coast signal generating unit generates a plurality of coast signals with pulse widths, each of which covers the different number of copy guard signals on the basis of the present copy guard signal in one frame signal of the 480p signal. The clock signal generating unit generates horizontal synchronization signals at the same period as that of the horizontal synchronization signals generated in a previous frame, while the corresponding coast signal is being generated.